# K.K.WAGH INSTITUTE OF ENGINEERING EDUCATION AND RESEARCH, NASHIK DEPARTMENT OF ELETRONICS AND TELECOMMUNICATION ENGINEERING

#### A.Y.2018-19

## SUMMARY TABLE FOR STAFF DEVELOPMENT PROGRAMMES (FACULTY DEVELOPMENT PROGRAMMES)

Sr.No	Name of faculty	Name of course
1.	Dr.Prof.D.M.	Hands on Training on "MACHINE LEARNING"
	Chandwadkar	
2.	Prof.Dr.S.A.	Two Days workshop state level workshop on
	Patil(Ugale)	"Blockchain Technology"
3.	Prof.M.P.Joshi	Faculty orientation workshop on Elective –IV, for
		B.E.E&TC revised syllabus 2015 course
4.	Prof.D.C.Shimpi	Faculty orientation workshop on Elective –III-
		PLC and Automation, for B.E.E&TC revised
		syllabus 2015 course
5.	Prof.P.J.Monde	Faculty orientation workshop on Mobile
		Communication, for B.E.E&TC revised syllabus
		2015 course

#### SUMMARY TABLE FOR STAFF HRD PROGRAMMES

Sr.No	Name of faculty	Name of course
1.	Prof.Deepali Chetan	Stress Management
	Shimpi	
2.	Prof.P.J.Monde	Introduction to Machine Learning
3.	Prof.P.J.Monde	Outcome Based Pedagogic Principles For
		Effective Teaching
4.	Prof.P.J.Monde	Effective Engineering Teaching In Practice

#### SUMMARY TABLE FOR STAFF SUMMER/WINTER WORSHOP

2018-19		
Sr.No	Name of faculty	Name of course
1.	Prof.P.J.Monde	NPTEL Workshop jointly conducted by
		MET,Nashik and IIT Madras

### SUMMARY TABLE FOR STAFF AS A RESOURSE PERSON

2018-19		
Sr.No	Name of faculty	Name of course
1.	Prof.Dr.D.M.	Expert Lecture on Power Electronic in Railway
	Chandwadkar	System for JS/SS/JAG Officers of Electrical
		Department
2.	Dr.S.S.Morade	Resource Person for Digital Signal Processing at
		KBTCOE,Nshik
3.	Prof.Dr.S.A.Patil	Expert Lecture on Power Electronic in Railway
	(Ugale)	System for JS/SS/JAG Officers of Electrical
		Department
4.	Prof.Dr.S.A.Patil	Resource Person for Expert Talk on "Application
	(Ugale)	of CRO" at E&TC Dept,KKWIEER,Nashik
5.	Prof.Dr.S.A.Patil	Resource Person for Expert Talk on
	(Ugale)	Programmable Logic Devices(S.E.E&TC) at
		KKWIEER
6.	Prof.Dr.S.A.Patil	Resource Person for Expert Talk "Application of
	(Ugale)	MOSFETs in VLSI" at E&TC
		Dept,KKWIEER,Nashik