



Department of Computer Engineering  
K. K. Wagh Institute of Engineering Education and Research  
Hirabai Haridas Vidyanagari, Amrut Dham, Panchavati, Nashik-422003

**Academic Year: 2018-19**

**Semester: I**

**Course Name: Mobile Communications**

**Class: BE**

**Division: A & B**

**Name of the Faculty: G. R. Gupta**

**Name of Method:** Flipped Learning

**Description:** Flipped learning is a pedagogical approach in which the conventional notion of classroom-based learning is inverted, so that students are introduced to the learning material before class. Students were asked to prepare a short summary about the topic and discuss during the lecture.

**Impact of Innovative Method:**

It helped students to understand the given topic in detail and share their thoughts about it and retain the concepts during examinations.



Department of Computer Engineering  
K. K. Wagh Institute of Engineering Education and Research  
Hirabai Haridas Vidyanagari, Amrut Dham, Panchavati, Nashik-422003

**Academic Year: 2018-19**

**Semester: I**

**Course Name: Skill Development Laboratory**

**Class: TE**

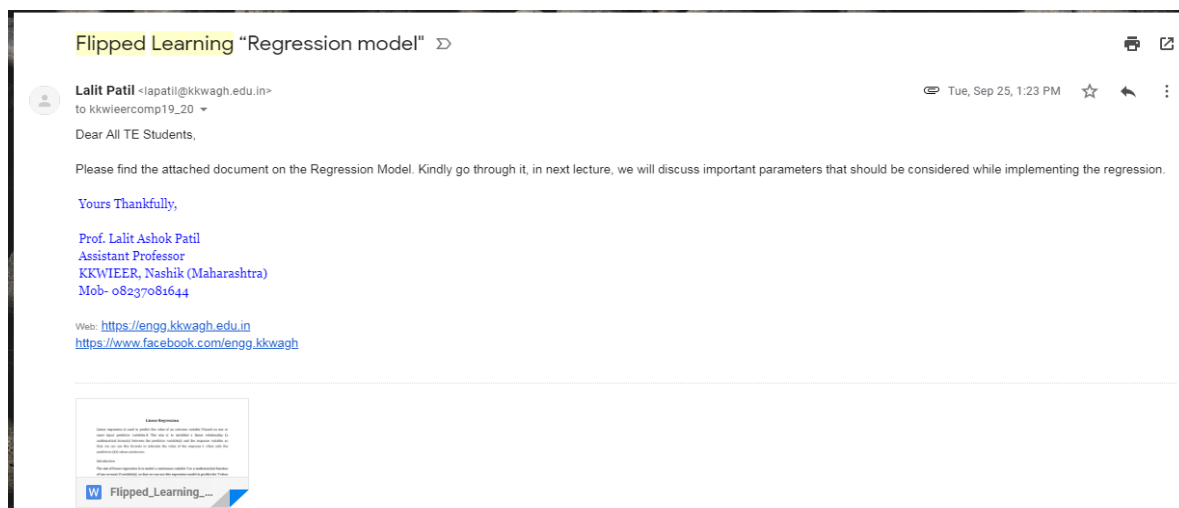
**Division: B**

**Name of the Faculty: L.A. Patil**

**Name of Method: Flipped Learning**

**Topic: Regression Model**

**Description:** A flipped Learning is a type of blended learning where the instructional content, is provided online, outside of the classroom. A document provided to students in advance on topic Regression Model. Then asked the students to go through the document and study the regression parameters in details. The discussion was held in the classroom in the next lecture.



**Impact of Innovative Method:**

It helped students actively participate in classroom and learned the concept at their own pace.



Department of Computer Engineering  
K. K. Wagh Institute of Engineering Education and Research  
Hirabai Haridas Vidyanagari, Amrut Dham, Panchavati, Nashik-422003

**Academic Year: 2017-18**

**Semesters: I**

**Course Name: Design and Analysis of Algorithms**

**Class: BE**

**Division: A & B**

**Name of the Faculty: Prof N. G. Sharma & Prof. R. H. Jadhav**

**Name of Method: Flip Learning**

**Description:** A new teaching Learning methodology was adopted in this semester called Flip Learning. For illustrating the concept of “Algorithm analysis”, a Decision tree algorithm was mailed to students in advance and they were asked to study the algorithm and comment on its time complexity. Further the discussion on same was conducted in the lecture on Friday 30<sup>th</sup> March 2018. It was very interactive and enjoyable session, as different views of different students were put forth. In the concluding session the correct analysis and complexity of the algorithm was explained by the Faculty.

**Impact of Innovative Method:**

It helped students understand the concept of time complexity and retain the method of computing the same during examinations.



Department of Computer Engineering  
K. K. Wagh Institute of Engineering Education and Research  
Hirabai Haridas Vidyanagari, Amrut Dham, Panchavati, Nashik-422003

**Academic Year:2017-18**

**Semester: II**

**Course Name: Microprocessor**

**Class: SE**

**Division: A & B**

**Name of the Faculty: N.M.Pagare & M. P. Mahajan**

**Name of Method: Flipped Learning**

**Description:** A flipped classroom is a type of blended learning where the instructional content, is provided online, outside of the classroom. PowerPoint presentation was provided to students in advance on Unit IV: Signal Diagram, Description of Signals and were asked to study and discuss later in classroom. Questionnaire was given in classroom and discussion was done on the same.

Few Questions discussed in the classroom:

1. 80386 is how many pin PGA?  
32 Pin
2. Size of data bus is \_\_\_\_\_  
32 bit
3. Which signals indicate which bytes of 32-bit data bus are involved with current transfer.  
Bytes enable signals (BE0 –BE3)
4. Which signal is used to request address pipelining?  
NA#
5. HOLD and HLDA pins belong to which signal?  
Bus arbitration signal
6. Which signal indicates a co processor request for data to be transferred by Intel 80386DX?  
PEREQ
7. Explain the function of BS16#  
It allows Intel 386 DX to directly connect 32-bit and 16-bit data buses.
8. What happens when PEREQ signal is asserted?  
It indicates the coprocessor request for a data operand to be transferred to/from memory by Intel386 DX. In response the Intel386 DX transfers information between the co processor and memory.
9. List different bus control signals.

ADS#, NA#, BS16#, Ready#

10. Which input pin indicates a request for interrupt service which cannot be masked by software?

NMI

**Impact of Innovative Method:**

It helped students actively participate in classroom and got deeper knowledge about the concepts.



Department of Computer Engineering  
K. K. Wagh Institute of Engineering Education and Research  
Hirabai Haridas Vidyanagari, Amrut Dham, Panchavati, Nashik-422003

**Academic Year: 2017-18**

**Semesters: II**

**Course Name: System Programming and Operating System**

**Class: TE**

**Division: A**

**Name of the Faculty: A. V. Kolapkar**

**Name of Method: Flipped Learning “Deadlock Avoidance Algorithms”**

**Description:** Flipped learning is a pedagogical approach in which the conventional notion of classroom-based learning is inverted, so that students are introduced to the learning material before class, with classroom time then being used to deepen understanding through discussion with peers and problem-solving activities facilitated by teachers.

**Flipped Learning “Deadlock Avoidance Algorithms”**

K.K.Wagh Education Society, Nashik Mail - Flipped Learning “Deadloc... <https://mail.google.com/mail/u/0/?ui=2&ik=3413a18f5b&jsver=LcPAST...>



Anand Kolapkar <avkolapkar@kkwagh.edu.in>

---

**Flipped Learning “Deadlock Avoidance Algorithms”**

Anand Kolapkar <avkolapkar@kkwagh.edu.in>  
To: kkwieercomp18\_19@googlegroups.com

Thu, Mar 22, 2018 at 5:09 PM

Dear Students,  
Please find attached a document giving details of “Deadlock Avoidance Algorithms”. Kindly go through the concepts, we will discuss and explore more details during next week lecture.

--  
Thanks and Regards

Anand Kolapkar  
Assistant Professor,  
Department of Computer Engineering,  
K.K.Wagh Institute of Engineering Education and Research,  
Nashik-422003  
Mob: 91-8275450764



Bankers Algorithm.ppt  
2017K

**Impact of Innovative Method:**

It helped students understand the concept of Deadlock and able to memorize the key terminologies and retain the concepts during examinations.



Department of Computer Engineering  
K. K. Wagh Institute of Engineering Education and Research  
Hirabai Haridas Vidyanagari, Amrut Dham, Panchavati, Nashik-422003

**Academic Year: 2017-18**

**Semesters: II**

**Course Name: Design and Analysis of Algorithms**

**Class: TE**

**Division: A & B**

**Name of the Faculty: Prof N. G. Sharma & Prof. R. H. Jadhav**

**Name of Method: Flip Learning**

**Description:** A new teaching Learning methodology was adopted in this semester called Flip Learning. For illustrating the concept of “Algorithm analysis”, a Decision tree algorithm was mailed to students in advance and they were asked to study the algorithm and comment on its time complexity. Further the discussion on same was conducted in the lecture on Friday 30<sup>th</sup> March 2018. It was very interactive and enjoyable session, as different views of different students were put forth. In the concluding session the correct analysis and complexity of the algorithm was explained by the Faculty.

**Impact of Innovative Method:**

It helped students understand the concept of time complexity and retain the method of computing the same during examinations.



Department of Computer Engineering  
K. K. Wagh Institute of Engineering Education and Research  
Hirabai Haridas Vidyanagari, Amrut Dham, Panchavati, Nashik-422003

**Academic Year: 2016-17**

**Semester: II**

**Course Name: High Performance Computing**

**Class: BE**

**Division: A**

**Name of the Faculty: Jyoti R Mankar**

**Name of Method: Flipped Learning**

**Description:** A flipped classroom is the type of blended learning where the instructional content, is provided online, outside of the classroom. PowerPoint presentation and research paper was provided to students in advance on Unit VI: HPC enabled advanced technologies. Questionnaire was given in classroom and discussion was done on the same.

Topics discussed in the classroom:

1. Petascale Computing
2. Optics in Parallel Computing
3. Quantum Computers
4. Power-aware Processing Techniques in HPC

**Impact of Innovative Method:**

It helped students actively participate in classroom and got deeper knowledge about the concepts.

**Note:** For feedback please email us at [njmetkar@kkwagh.edu.in](mailto:njmetkar@kkwagh.edu.in)