

“Two Week ISTE STTP on CMOS, Mixed Signal & Radio Frequency VLSI Design”

The Department of Electronics & Telecommunication had conducted the “Two Week ISTE STTP on CMOS, Mixed Signal & Radio Frequency VLSI Design” under “T10KT” program of NMEICT from 30 January to 4 February 2017. Topics such as design & implementation methodology of analog front-end, data converters, digital VLSI, RF circuits & transceiver were covered during this STTP. Around 21 faculty members from various engineering colleges & polytechnics had completed this STTP successfully. Principal Prof. Dr. K. N. Nandurkar had emphasized the importance of MOOCs while Prof. Dr. D. M. Chandwadkar, HOD (E & TC) had underlined the importance of the life-long learning during there interaction with participants. Prof. M. R. Pardeshi worked as remote centre co-ordinator, Prof. P. J. Mondhe worked as workshop co-ordinator & Prof. V. R. Takate worked as teaching assistant for this STTP.

