



Department of Electronics and Telecommunication Engineering
K. K. Wagh Institute of Engineering Education and Research
Hirabai Haridas Vidyanagari, Amrut Dham, Panchavati, Nashik-422003

Savitribai Phule Pune University
Sponsored
Two Day National Level Workshop
on

“Design, Simulation & Implementation of Signal Processing Applications”

Date: 3/1/2019 to 4/1/2019

Venue: ME Research Lab, E&TC department.

Grant Sanction from SPPU (Under Quality Improvement Program): Rs.2,00,000/-

Resource Person:

Mr. Gurudutt Talwar
Senior FPGA Design Engineer,
Dexcel Electronics Designs Pvt. Ltd., Bengaluru

Mr. Prasad Kalane
Application Engineer (Signal Processing),
Anubhuti Research & Development Center, Pune

Mr. Suraj Gayake
Application Engineer (Embedded &Iot).
SSP Technology, Pune

Mr. Anand Bhavsar
Application Engineer (Embedded).
Microwind Engineering Solutions, Nashik

Participants:

Faculty members, industry persons, researchers and post graduate and graduate students.

Number of Participants: 45

Objectives of Workshop

- To introduce the basic concepts of signal processing.
- To tackle the problems of large data handling.
- To discover structure inside unstructured compressed data.
- To explore the recent developments and research issue in the field of Fourier Transform, Short Time Fourier Transform & Wavelet Transform.



Contents of Workshop

Basic Signal Processing:

- Introduction to Signal Processing.
- Data Acquisition in MATLAB & Python.
- Data Visualisation in MATLAB & Python.
- Data Pre-processing in MATLAB.
- Spectral Analysis of Signal.
- Statistical Signal Processing.

Advance Signal Processing:

- Introduction and Background.
- Review of Fourier Theory & Short Time Fourier Transform.
- Python programming and implementation in Raspberry Pi.

Applications (Hands on):

- Image Processing.
- Speech Recognition.
- Signal Denoising.
- Data Mining / Feature Extraction Classification.

Photographs:





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in association with

Savitribai Phule Pune University

Organized

Two Day National Level Workshop on

"Design, Simulation & Implementation of Signal Processing Applications"

on 3rd & 4th January 2019

Certificate

This is to certify that Mr./ Ms. _____
of _____ has attended the Two Day National Level Workshop
on "Design, Simulation & Implementation of Signal Processing Applications" organized by Department of
E&TC Engineering of K. K. Wagh IEER in association with Savitribai Phule Pune University on 3rd & 4th
January, 2019.

Prof. S. P. Munot (Bhabad)
Workshop Coordinator

Prof. Dr. D. M. Chandwadkar
Head, Department of E&TC Engg.

Prof. Dr. K. N. Nandurkar
Principal, KKWIEER, Nashik