

Resume



Name: Ms. Mrunal Vasant Marathe

Designation: Assistant Professor (Electronics & Telecommunication Engineering)

Date of Birth : 22nd February 1997

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Educational Qualification:

Examination	Institute	University	Year	Percentage
ME(VLSI & Embedded Systems)	K.K.Wagh College, Nashik	SPPU	2020	8.53 CGPA
BE (E&TC)	NDMVP's KBTCOE	SPPU	2018	65 %
HSC	R.Y.K College, Nashik	State Board	2014	71.08 %
SSC	Day Care Centre	State Board	2012	87.20 %

Experience: Working as an Assistant Professor at K.K.W.E.E.R (From March 2021)

Subjects Taught: Testing and Verification of VLSI Circuits, Analog CMOS Design etc.

Academic Projects:

- ❖ Implementation of drive mode feature for PHEV vehicle using IBMHarmony process.
- ❖ Controlling flow of oil in tire building process using PLC Programming
- ❖ Street light glows on detection of moving vehicle.

Papers Published:

- ❖ Presented a technical paper “Implementation of Drive Mode Feature for PHEV Vehicle using IBM Harmony Process” in ePGPEX 2020 organized by Board of Studies (E&TC) in association with Pimpri Chinchwad College of Engineering , Pune, July 2020
- ❖ Published a Research paper “Implementation of Drive Mode Feature for PHEV Vehicle using IBM Harmony Process” in International Journal of Engineering Research and Technology, June 2021