K.K.Wagh Institute of Engineering Education and Research

Hirabai Haridas Vidyanagari, Mumbai Agra Road Amrutdham, Panchavati, Nashik, Maharashtra 422003

Minutes of Alumni Interaction

29th Aug 2020 / 10:30 AM / GoToWebinar App



ATTENDEES:

- Mr. Jagdish A. Ugale(Guest)
- Proff. Dr. D. M. Chandwadkar (HoD)
- Proff. Dr. Sunita Patil (Ugale)
- Staff of E&TC
- Students of E&TC

AGENDA:

• To introduce students with the importance of **Project Management**

• To guide students the steps to be followed for developing any project

The following points were discussed:

- How to select a project
- Design/Development steps
- Application level design on paper
- Hardware level design on paper
- Component selection criteria
- Microcontroller level design on paper
- Physical level Implementation
- Testing/Validation
- Importance of two semester period allotted to students for Major Project
- Importance of both software and hardware knowledge for project development
- How to schedule a plan for accomplishing various project works
- Question/Answer session