

# K.K.Wagh Institute of Engineering Education and Research

Hirabai Haridas Vidyanagari, Mumbai Agra Road Amrutdham, Panchavati, Nashik, Maharashtra 422003

## Minutes of Alumni Interaction

10th Sep 2020 / 11:00 AM / GoToWebinar App

K. K. Wagh Education Society's  
**K. K. Wagh Institute of Engineering Education & Research, Nashik**  
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(Affiliated To SavitribaiPhule Pune University And Approved By AICTE, New Delhi)

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**Department of Electronics & Telecommunication Engineering**  
Organizes  
**Online Webinar on**  
**“ FPGA Design using Xilinx Vivado Suite”**  
By  
**Mr. Hemant Kumar**  
Application Engineer, CoreEL Technologies, Bangalore

  
**Mr. Hemant Kumar**

On 10<sup>th</sup> September 2020 from 11.00 AM to 1.00 PM  
— Meeting details: —  
<https://attendee.gotowebinar.com/register/5878127010465766155>

### ATTENDEES :

- Mr. Hemant Kumar(Guest)
- Proff. Dr. D. M. Chandwadkar (HoD)
- Proff. Dr. Sunita Patil (Ugale)
- Staff of E&TC
- Students of E&TC

### AGENDA :

- To introduce students with **FPGA Design using Xilinx Vivado Suite**

## **The following points were discussed :**

- What is FPGA
- Difference between FPGA and ASIC
- Applications of FPGA
- 7 series FPGA architecture and features:
  - Configurable Logic Block
  - Wide Multiplexers Block
  - Slice Flip-Flop Capabilities
  - SLICEM Used as a Distributed SelectRAM Memory
  - I/O Types of 7 series FPGA
  - Clocking Resources
- About Xilinx and Vivado Design Suite
- How to install and use the features of Xilinx Vivado software
- Showed an example of how to model a digital circuit using Xilinx Vivado
- Question/Answer Session